



2013 International Conference on Analog VLSI Circuits

Montréal, Canada
October 16 - 18, 2013

Final Call for Papers

Submission due date: June 24, 2013

Organization

General Chair

Roni Khazaka, *McGill University, Canada*

General Co-Chairs

Takeshi Shima, *Kanagawa University, Japan*

Mounir Boukadoum, *UQAM, Canada*

TPC Chair

Yasuaki Inoue, *Waseda University, Japan*

TPC Secretary

Cosy Muto, *Nagasaki University, Japan*

Publication Chair

Frederic Nabki, *UQAM, Canada*

Invited speakers

Hirofumi Shinohara, *STARC, Japan*

Mohammed Ismail, *Ohio State University, USA
and more.*

Conference venue

McGill Faculty Club and Conference Centre

3450 McTavish Street, Montreal

<http://www.mcgill.ca/facultyclub/>

Important dates

Deadline of paper submission **June 24, 2013**

Notification of acceptance August 5, 2013

Camera ready manuscript August 30, 2013

Deadline for author registration August 30, 2013

Early registration August 15, 2013

WEB sites and contact addresses

Main web site: <http://www.avic2013.org/>

Submission: <http://opamp.ss.titech.ac.jp/avic/>

General contact: avicconference@gmail.com

TPC contact: avic2013tpc@gmail.com

Aims & Scope

The Research Committee on Electronic Circuits of the Institute of Electrical Engineers of Japan (IEEJ) is sponsoring and cooperating with IEEE Montreal Section to hold the 2013 International Conference on Analog VLSI Circuits in Montreal, Canada, on October 16–18, 2013.

The purpose of the conference is to exchange information, ideas and recent research results on analog VLSI circuits and their applications.

This workshop has formed a long-standing community for various analog VLSI circuit specialists from both industry and academia worldwide, and promoted active brainstorming among them at the site.

The Conference will include Social Events and Invited Speakers in order to encourage knowledge sharing and networking in a relaxed and fruitful atmosphere.

A selection of the best papers will be published in a SPRINGER journal special issue.

Main Topics

Topics preferred for the conference are listed below, but not limited to:

Analog and Mixed Analog-Digital Integrated Circuits

Amplifiers, Filters, Comparators, Oscillators, Multipliers, PLL, Voltage/Current References, Sample-And-Hold Circuits, A/D and D/A Converters, High speed IO Interface, DC-DC converters

Analog VLSI Design Automation

Layout Techniques, Simulation Techniques, AHDL, Analog IP

Analog Signal and Information Processing Applications

Telecommunication, Multimedia, Automotive Electronics, Biomedical Electronics, Consumer Electronics, Neural Networks, Sensing and Sensor Networks, Space and Military Electronics

Submission

Authors are requested/asked to send their manuscripts written in English within 4-6 pages, along with information which includes the complete title, author name(s), affiliation(s) of author(s), and corresponding author (name, postal and e-mail addresses, telephone/fax numbers), via online submission at

<http://opamp.ss.titech.ac.jp/avic/>

by June 24, 2013. The format details are described on the submission WEB site.

After peer reviewing by experts, notification of acceptance will be sent via e-mail by August 5, 2013.

Camera ready manuscript should be prepared in the conference format which is provided on the web site.

The deadline for camera-ready manuscript submission is August 30, 2013.

At least one of the authors must register by August 30, 2013. Otherwise the paper will not be included in the conference proceedings. **It is mandatory that all accepted papers must be presented at the conference.**



The Institute of Electrical Engineers of Japan



IEEE



Montréal

