

AVIC2013 Conference Program (tentative)

Wednesday, October 16, 2013

0800–1700 Registration Desk Open

0900–0950 Opening Ceremony

MC: , Canada

Welcome Address by General Chair, *Roni Khazaka, McGill University, Canada*

Opening Address by General Co-Chair, *Takeshi Shima, Kanagawa University, Japan*

Opening Address by General Co-Chair, *Mounir Boukadoum,*

Université du Québec à Montréal, Canada

Opening Address by TPC Chair, *Yasuaki Inoue, Waseda University, Japan*

1000–1100 Invited Talk 1

Session Chair: Yasuaki Inoue, Waseda University, Japan

“Self-Healing” Design of RF and mm-Wave Systems on Chip

Prof. Mohammed Ismail, Ohio State University, USA

1100–1230 RF Circuits

Session Chairs: Cosy Muto, Nagasaki University, Japan

, Canada

Spiral Inductor Evaluation For LC-Voltage Controlled Oscillators in 0.18mm CMOS

Ryo Ito, Kazuyuki Wada and Kawori Sekine

Dual-Band Class-E Power Amplifier with Varying Duty-Ratio

Takuma Torii, Akira Hyogo and Toshiro Tsukada

A 0.18mm CMOS Fully Differential VGA with 47dB Dynamic Range

I. L. Abdalla, H. Jia, R. Pokharel, A. Allam, M. Ragab, H. Kanaya and K. Yoshida

1230–1330 Lunch

1330–1530 Analog Circuits, Applications and Related Topics I

Session Chairs: Akira Yasuda, Hosei University, Japan

, Canada

Design Method of a Control Circuit for 0.6-V CMOS Log-Domain Filter Extended

Temperature Range

Takahiro Ishihara, Kazuyuki Wada and Kawori Sekine

LSI Design for Patch Clamp Measurement Using 0.18mm CMOS Process

Wataru Nakayama, Yohei Yasuda, Takuya Kawashima and Nobuhiko Nakano

A Study on CPG Device with Interstitial Cells Model by 0.18mm CMOS Process

Katsutoshi SAEKI, Daisuke NIHEI, Tatsuya TATEBE and Yoshifumi SEKINE

A 0.5V Analog Differential Absolute Distance Circuit using Pulse Width Modulation and

Multi-input Comparator in Weak Inversion Region

Tomochika Harada

1530–1600 Coffee Break

1600–1700 Power Electronics

*Session Chairs: Nobukazu Takai, Gunma University, Japan
, Canada*

A Wide Input Amplitude Range, Highly Efficient Rectifier For Energy Harvesting Systems

Qiang Li, Dan Niu and Yasuaki Inoue

Buck Converter with Fast Output Voltage Transition using Current Regeneration Path

Takahide Sato and Daisuke Kanemoto

Thursday, October 17, 2013

0800–1730 Registration Desk Open

0900–1000 Invited Talk 2

Session Chairs: Takeshi Shima, Kanagawa University, Japan

Extremely Low Power Digital and Analog Circuits

Dr. Hirofumi Shinohara, STARC, Japan

1000–1030 Break

1030–1230 AD/DA and Related Topics I

*Session Chairs: Daisuke Kanemoto, Yamanashi University, Japan
, Canada*

A Low-Offset-Voltage Dynamic Comparator using Automatic Differential-Pair Matching for Low-Power ADCs

Takayuki Okazawa, Ippei Akita and Makoto Ishida

A beta-expansion Based 10-bit CMOS Cycle ADC with Radix-value Self-correction Technique

R.Sugawara R.Suzuki H.San T.Matsuura K.Aihara and M.Hotta

Robustness of Cyclic ADC Based on b-expansion

*Toshiki Yamada, Ronpei Sugawara, Hao San, Tatsuji Matsuura,
Kazuyuki Aihara and Masao Hotta*

Background Calibration Technique for a Pipelined ADC Using a Noise-Shaping and Feedback Structure

Takahisa Kawabe, Michitaka Yoshino and Akira Yasuda

1230–1330 Lunch

1330–1500 AD/DA and Related Topics II

*Session Chairs: Hao San, Tokyo City University, Japan
, Canada*

A Delta-Sigma Modulator with a FIR Filter Reducing Quantization Noise in Signal-band

Satoshi Saikatsu, Michitaka Yoshino and Akira Yasuda

Analysis of Flash A/D Converter Gain Effect in Delta-Sigma Modulators

Daisuke Kanemoto, Takahide Sato and Makoto Ohki

Delta Sigma ADC for GSM Communication

*Anugerah Firdauzi, Muhammad Arijal, Kiki Wirianto, Amy H. Salman
and Achmad F. Mas'ud*

1500–1530 Coffee Break

1530–1730 Special Session

Organizer: , Canada

Session Chairs: , Canada

1800–2030 Conference Banquet (including Best Paper Award Ceremony)

Friday, October 18, 2013

0800–1730 Registration Desk Open

0900–1000 Invited Talk 3

Session Chairs: Roni Khazaka, McGill University, Canada

{Title not known yet}

Prof. Gordon W. Roberts, McGill University, Canada

1000–1030 Coffee Break

1030–1230 Student Poster Session

Organizer: , Canada

Session Chairs: , Canada

1230–1330 Lunch

1330–1530 Analog Circuits, Applications and Related Topics II

*Session Chairs: Kawori Sekine, Meiji University, Japan
, Canada*

Probabilistic Search Schemes for High-Speed Low-Power Content-Addressable Memories

Naoya Onizawa, Shoun Matsunaga, V. C. Gaudet, W. J. Gross and T. Hanyu

DC Characteristics and Variability on 90nm CMOS Transistor Array-style Analog Layout

Shigetoshi Nakatake and Toru Fujimura

A New Technique for Improving Sensitivity of Quartz Crystal Microbalance based on Phase Locked Loop

Shohei Yamada, Yousuke Kaneko, Takeshi Imai and Yukinori Sakuta

Current-to-Time Converter Circuit for High Precision System containing Hysteresis Characteristics

Toru Kamiyama, Nobukazu Takai, Haruo Kobayashi and Biswas Sumit Kumar

1530–1630 Closing and Farewell Ceremony

MC: