CALL FOR PAPERS

The 6th International Conference on Integrated Circuits, Design, and Verification (ICDV 2015)

August 10 - 11, 2015 – Ho Chi Minh city, Vietnam (http://www.uet.vnu.edu.vn/icdv)

Organized in conjunction with VJMW 2015

Sponsored by
IEICE Vietnam Section,
IEICE Electronics Society Technical Committee on Integrated Circuits and Devices,
and IEICE Engineering Science Society Technical Committee on VLSI Design Technologies

Participating-Sponsored by
IEICE Electronics Society Technical Committee on Microwave Engineering,
IEEE SSCS Japan Chapter, IEEE SSCS Kansai Chapter, IEEE SSCS Vietnam Chapter,
and The Radio-Electronics Association of Vietnam (REV)

The IEICE International Conference on Integrated Circuits, Design, and Verification (ICDV) provides an annual forum for exchanging ideas, discussing research results, and presenting chips, circuit designs and applications in solid-state and semiconductor fields. Continuous scaling of the CMOS devices increases the number of transistors on a VLSI chip. It will soon reach the level of 10 giga transistors on a single chip, which is equivalent to the total neuron numbers in the human brain. This would certainly provide us a great opportunity for new applications and information processing. On the other hands, we now live in a world where every device is rapidly becoming connected. The era of the Internet of Things (IoT) changes drastically the way people and thinks communicate and work together in intelligent processes. To discuss utilizing the scaling advantages and their applications to the IoT era, we call for contributions about new proposal of application systems, VLSI architectures, and design methodologies as well as the technologies in the integrated circuits and devices field. We expect to this conference explores and stimulates the contributed researches to those subjects. The papers are solicited from prospective authors interested in the related fields.

The ICDV 2015 conference is sponsored by the IEICE Vietnam Section, the IEICE ICD technical group and the IEICE VLD technical group, hosted by HCM University of Sciences and VNU University of Engineering and Technology (VNU-UET), and will be held in Ho Chi Minh city, Vietnam. Further details on the conference, paper submission guidelines, and templates will be updated on the ICDV website at: http://www.uet.vnu.edu.vn/icdv

Paper Submission

Prospective authors are invited to submit full-length, six-page manuscripts, including figures, tables and references, to the official ICDV 2015 website. All papers will be handled electronically. Accepted and presented papers will be included in the conference proceedings published by the IEICE with ISBN (the copyright shall be assigned to the IEICE) and will be invited to re-submit an extension to high quality peer-reviewed journals: REV Journal on Electronics and Communications (JEC) and VNU Journal of Computer Science and Communication Engineering (JCSCE). Papers are solicited in the following categories, but are not limited to:

1. Digital integrated circuits and signal processing
2. Processors/ multiprocessors
3. Memory
4. Analog and mixed-signal circuits
5. RF integrated circuits and microwave engineering
6. Circuit technologies
7. Design experience with advanced design technology
8. Circuits/devices modeling, verification and testing
9. Reconfigurable architectures, FPGA-based designs
10. Embedded systems design
11. Applications related to integrated circuits and devices
12. Internet-of-Things applications

Important dates

- Full manuscript submission: June 15, 2015
- Notification of acceptance: July 20, 2015
- Camera-ready submission: July 31, 2015

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